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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/072,415
Filing Date	February 5, 2002
Inventor	Ji Ung Lee et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2891
Examiner	V. Yevsikov
Attornev's Docket No	MI30-068
Title: Field Effect Transistor Fabrication	Methods, Field Emission Device Fabrication
Methods, and Field Emission Device	

Assistant Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

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1. Interview Summary

2. Transmittal Form (PTO/SB/21)

Dated:  $\frac{2/15/2006}{N}$  By:  $\frac{1}{N}$ 

Natatie King Telephone No. (509) 624-4276 Facsimile No. (509) 838-3424

NUMBER OF PAGES IN FACSIMILE:

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PTO/SB/21 (03-03)
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	Application Number	10/072,415
TRANSMITTAL	Filing Date	Feb. 5, 2002
FORM	First Named Inventor	Ji Ung Lee et al.
(to be used for all correspondence after initial filing)	Art Unit	2891
	Examiner Name	V. Yevsikov
Total Number of Pages in This Submission	Attorney Docket Number	MI30-068
ENCLOSURES (Check all that apply)		
Fee Transmittal Form Fee Attached Amendment/Reply After Final	Drawing(s)  Licensing-related Papers  Petition  Petition to Convert to a Provisional Application	to a Technology Center (TC)  Appeal Communication to Board of Appeals and Interferences  Appeal Communication to TC (Appeal Notice, Brief, Roply Brief)  Proprietary Information
Affidavits/declaration(s)  Extension of Time Request  Express Abandonment Request  Information Disclosure Statement	Power of Attorney, Revocation Change of Correspondence Addre Terminal Disclaimer Request for Refund CD, Number of CD(s)	Status Letter Other Enclosure(s) (please Identify below): Interview Summary
Certifled Copy of Priority Document(s)  Response to Missing Parts/ Incomplete Application  Response to Missing Parts under 37 CFR 1.52 or 1.53	emarks .	
SIGNATUR	E OF APPLICANT, ATTORNI	EY, OR AGENT
Firm or James D. Shaurette, Reg. No. 3 Wells St. John, P.S. Individual Signature Date	99,833	
CERT	IFICATE OF TRANSMISSION	/MAILING
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:		
Typed or printed Natalia King		
Signature		Date 2/15/86
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Filing Date February 5, 2002
Inventor
Assignee Micron Technology, Inc.
Group Art Unit
Examiner V. Yevsikov
Confirmation No
Customer No
Attorney's Docket No
Title: Field Effect Transistor Fabrication Methods, Field Emission Device Fabrication
Methods, and Field Emission Device Operational Methods

#### **INTERVIEW SUMMARY**

To:

**Assistant Commissioner for Patents** 

P.O. Box 1450

Alexandria VA 22313-1450

From:

James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)

Wells, St. John, P.S.

601 W. First Avenue, Suite 1300 Spokane, WA 99201-3828

Sir:

Applicants received a final Office Action in the above-identified application dated July 15, 2005. Applicants filed a timely response on September 15, 2005 which attempted to place the application in condition for allowance. The Examiner indicated via telephone conference on October 12, 2005 to the undersigned that the amendment was approved and the application was being processed for allowance. No allowance was received by Applicants and the undersigned spoke with Examiner Baumeister on January 17, 2006 who indicated that previously allowed subject matter was no longer allowable, the final Office Action would be withdrawn and a new non-final Office Action would be forthcoming.

The Examiner is requested to phone the undersigned if the Examiner believes such would facilitate prosecution of the present application. The undersigned is available for telephone consultation at any time during normal business hours (Pacific Time Zone).

Respectfully submitted,

Reg. No. 39,833

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